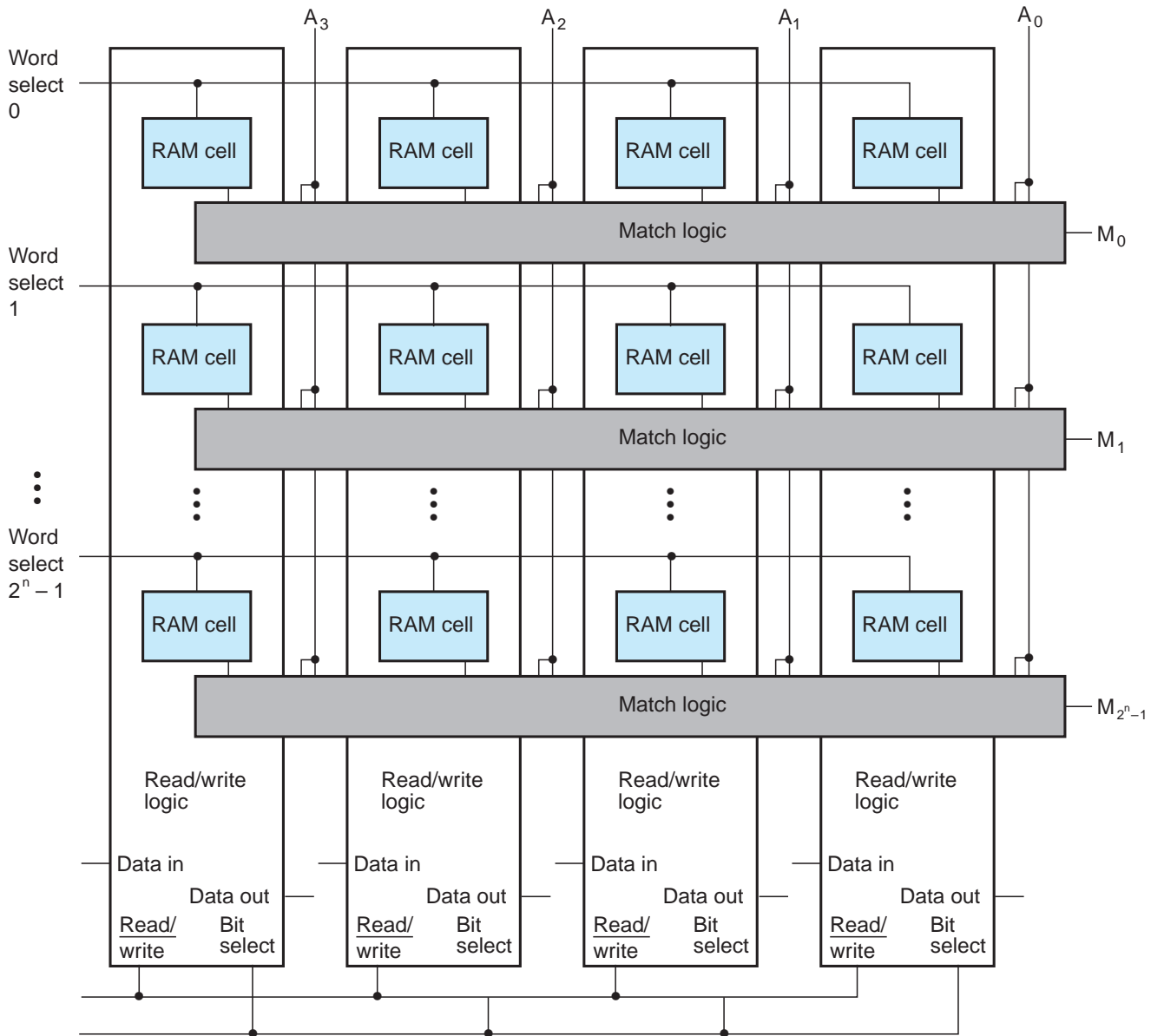
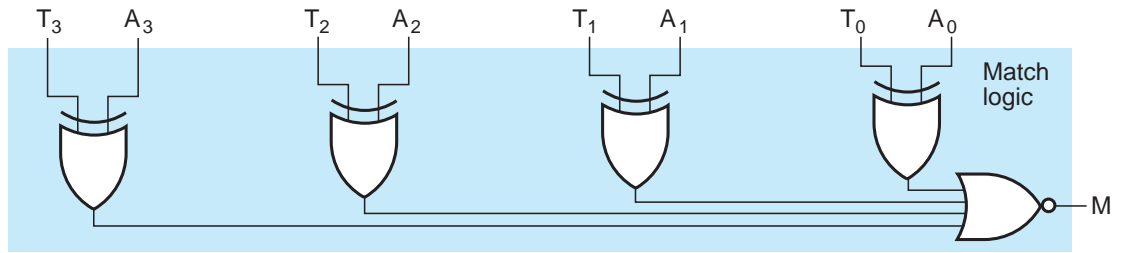
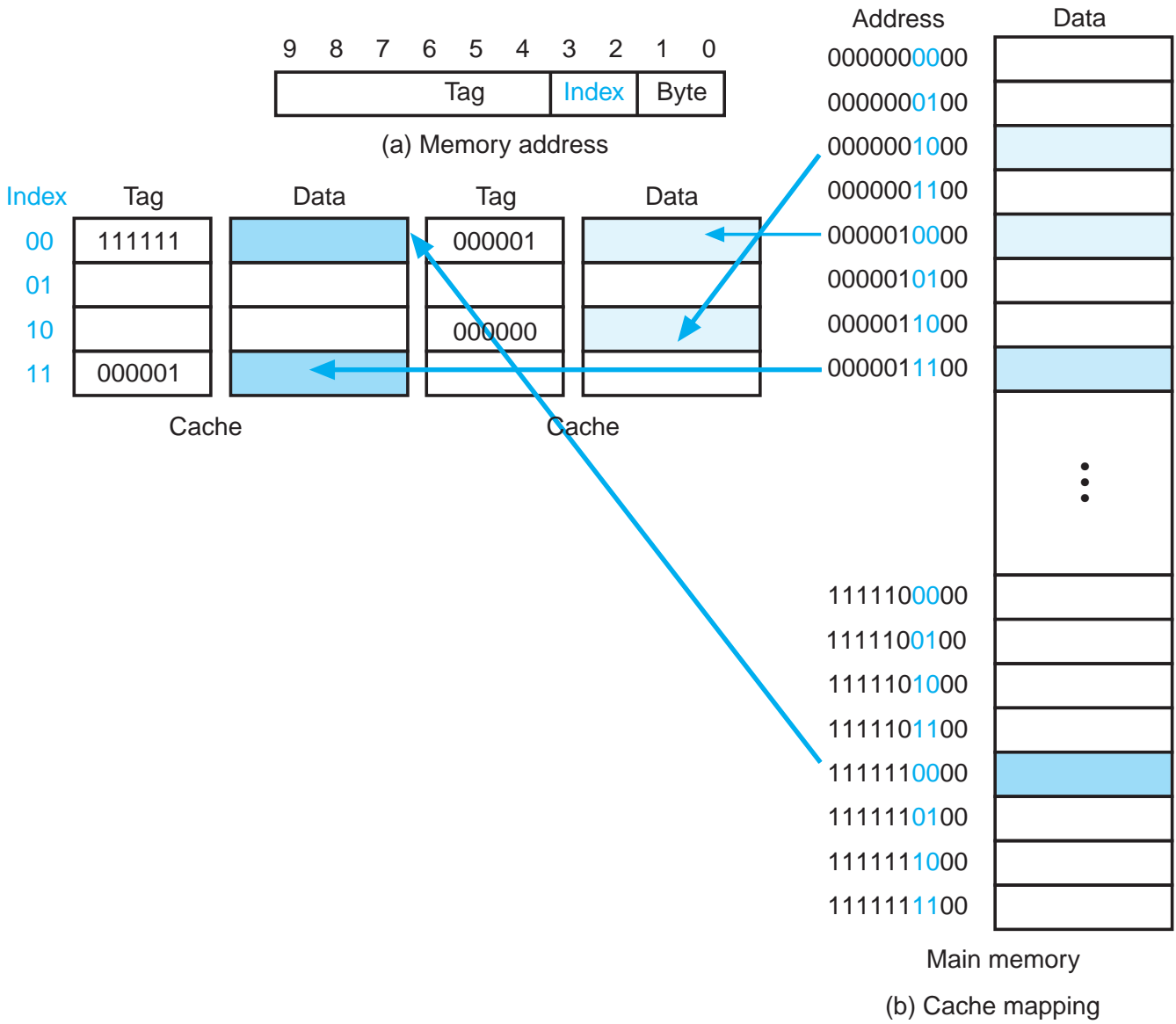


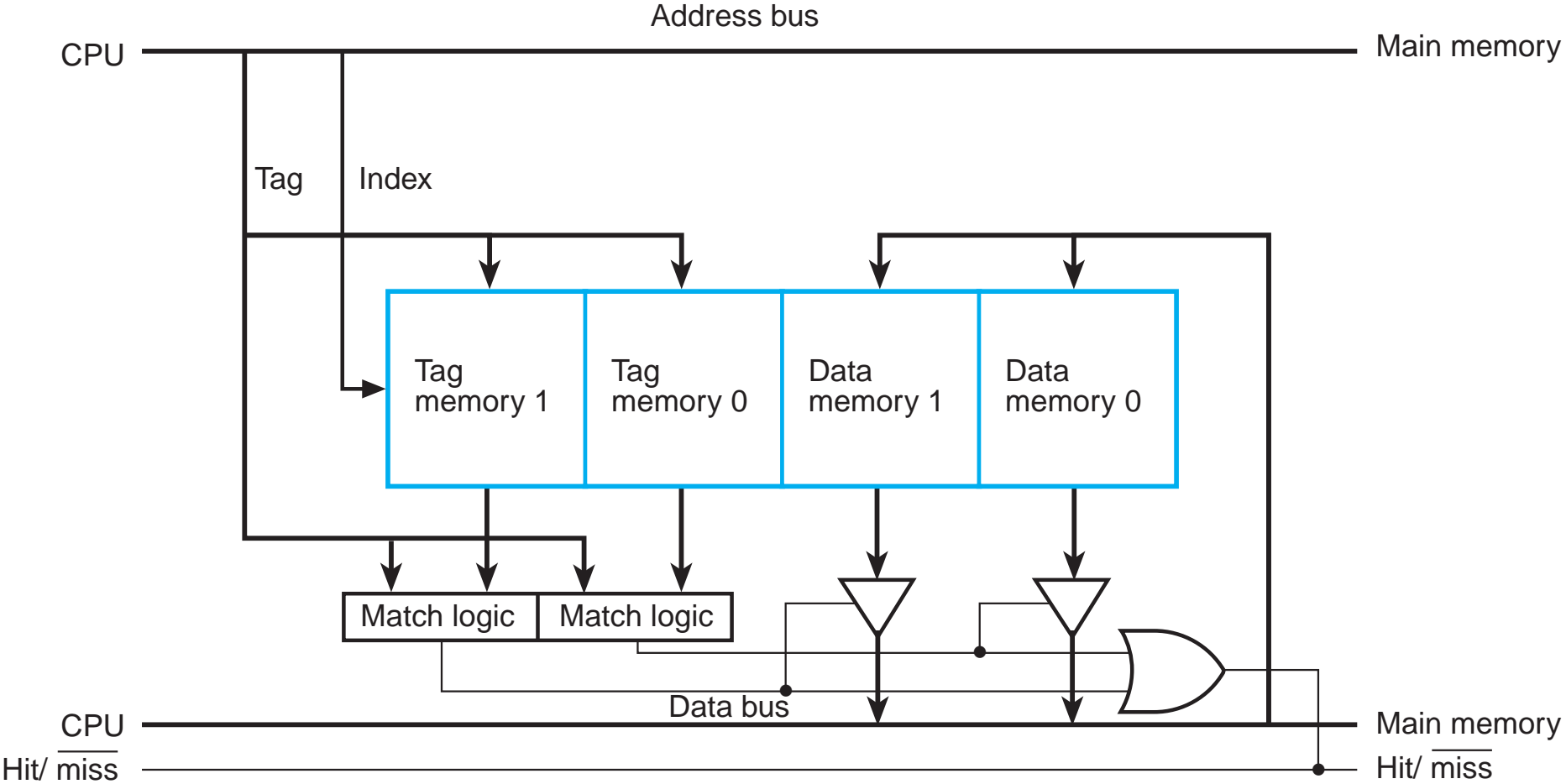
Associative Memory for 4-bit Tags

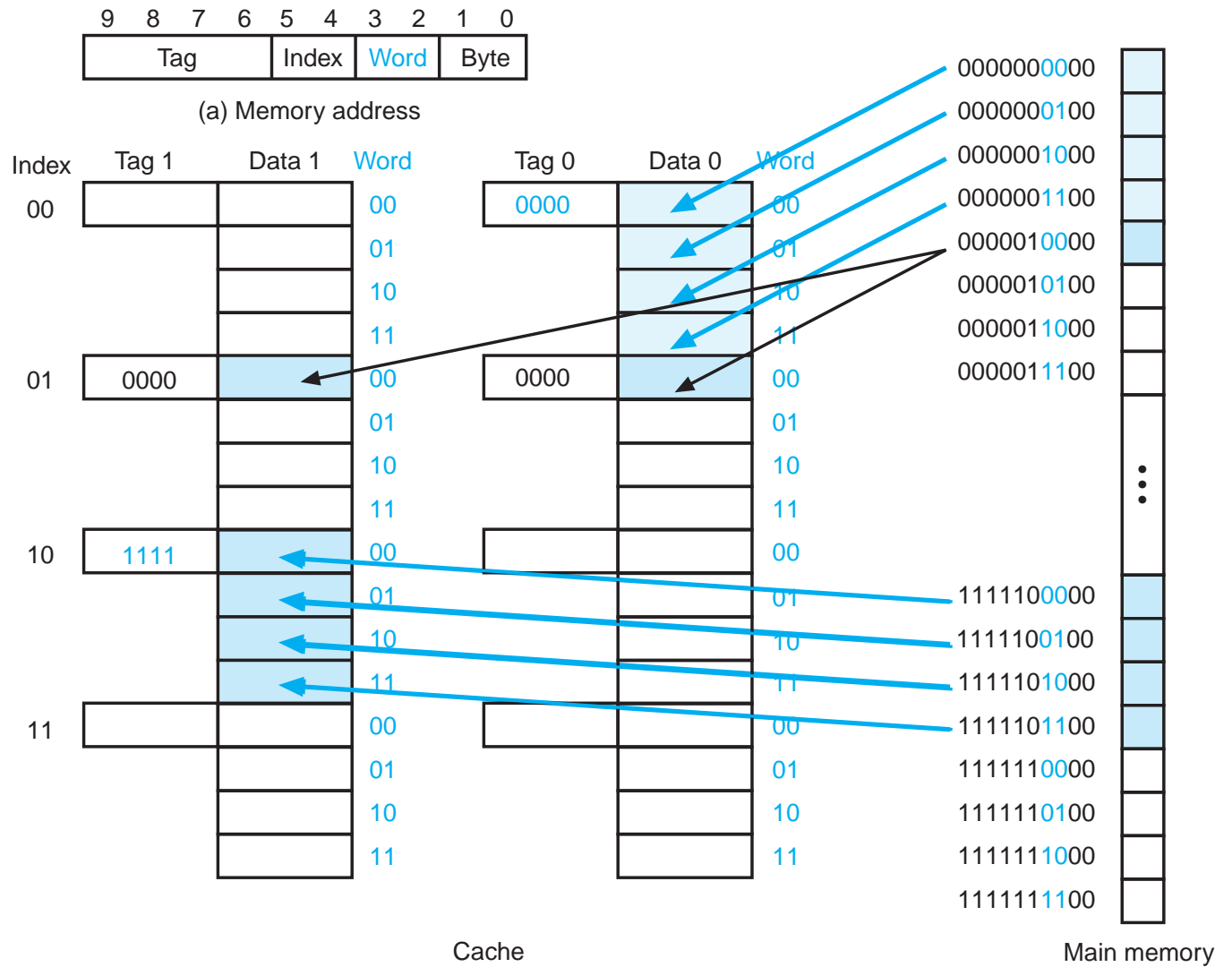


Two-way Set-associative Cache

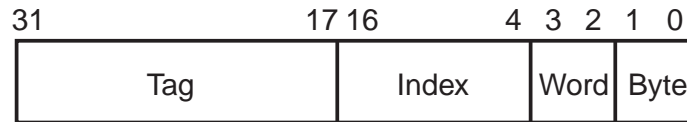


Partial Hardware Block Diagram for Set-associative Cache

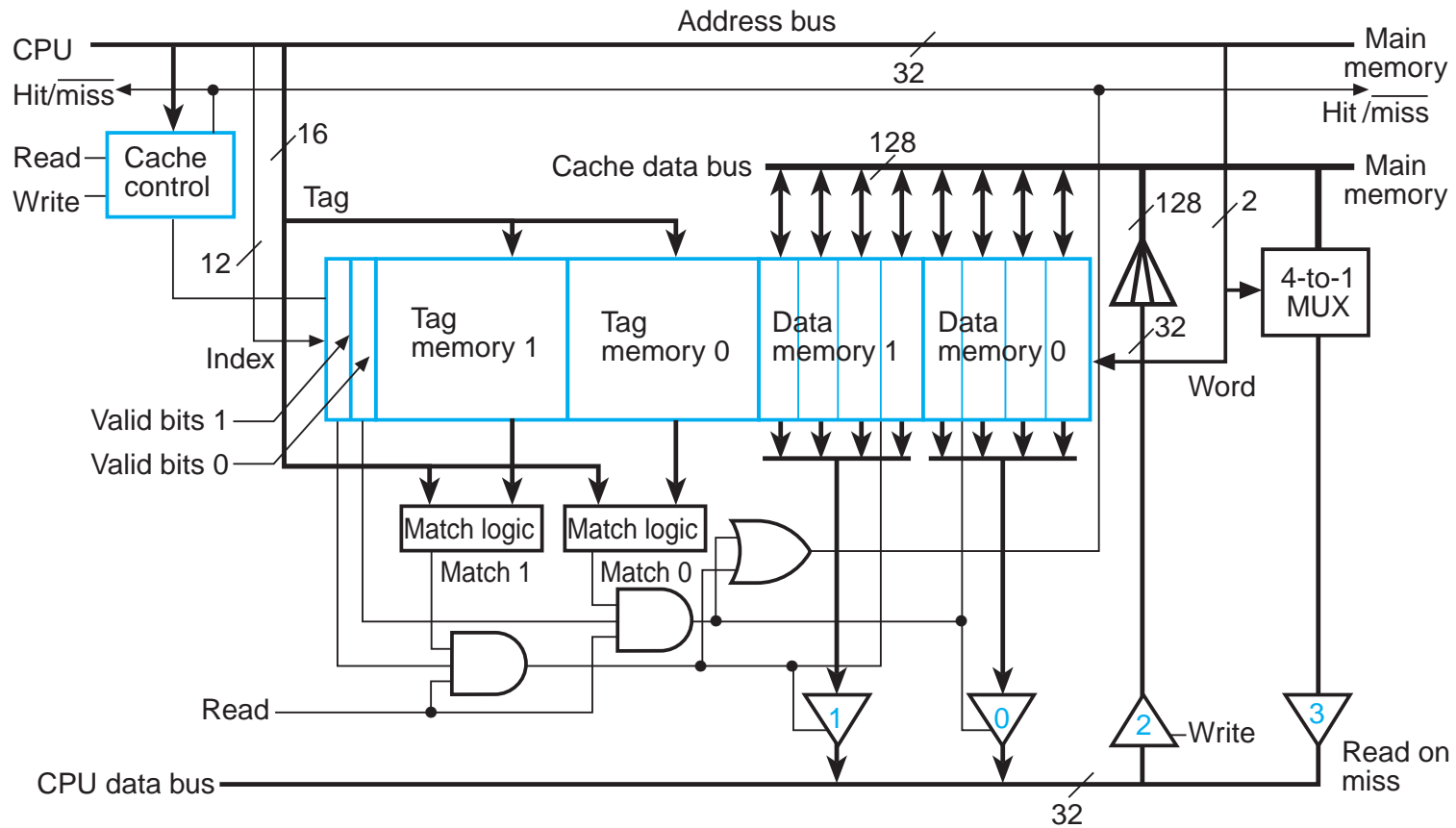




Detailed Block Diagram for 256K Cache

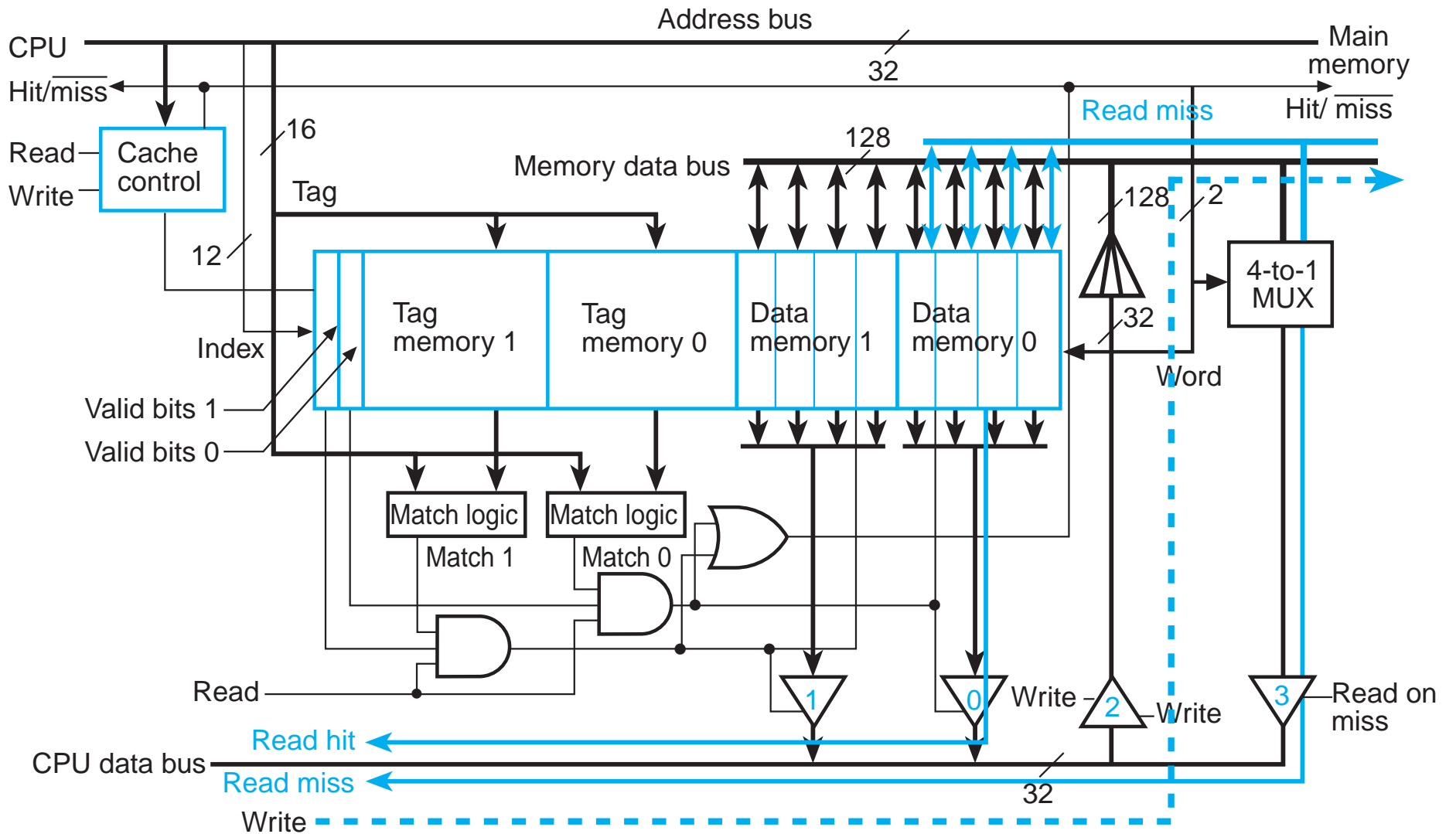


(a) Memory address

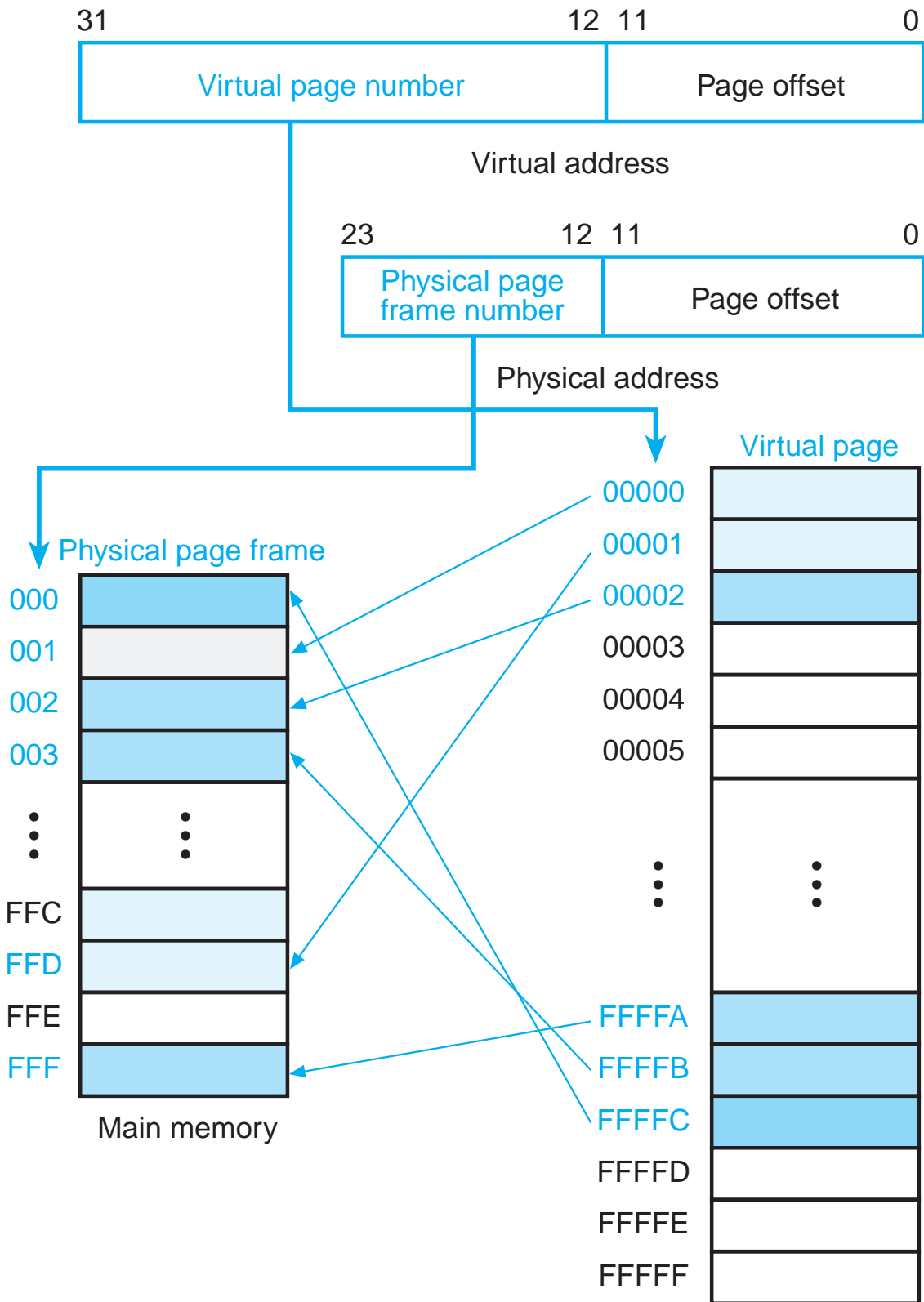


(b) Cache diagram

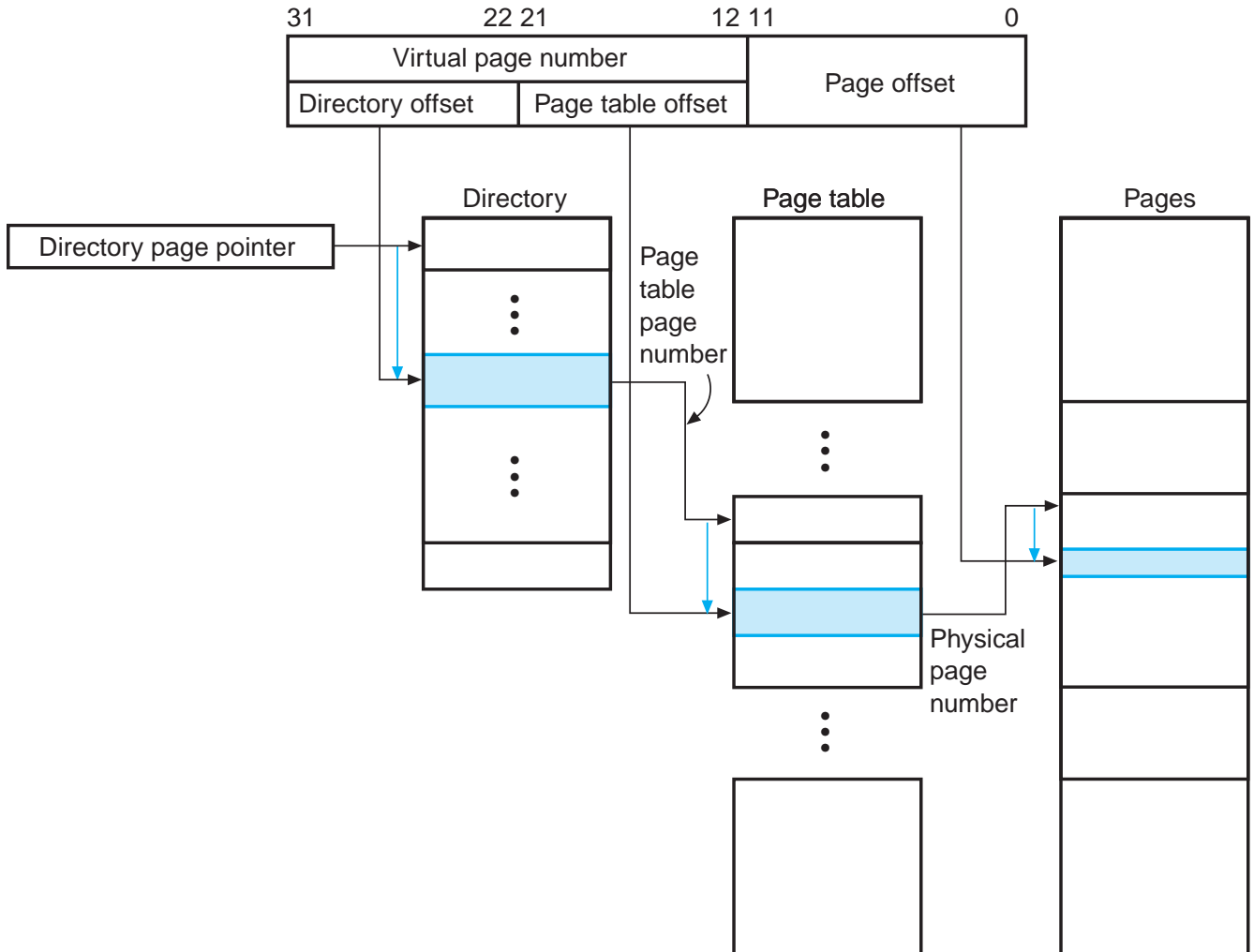
256K Cache: Read and Write Operations



Virtual and Physical Address Fields and Mapping



Example of Page Table Structure



Example of Translation Lookaside Buffer

